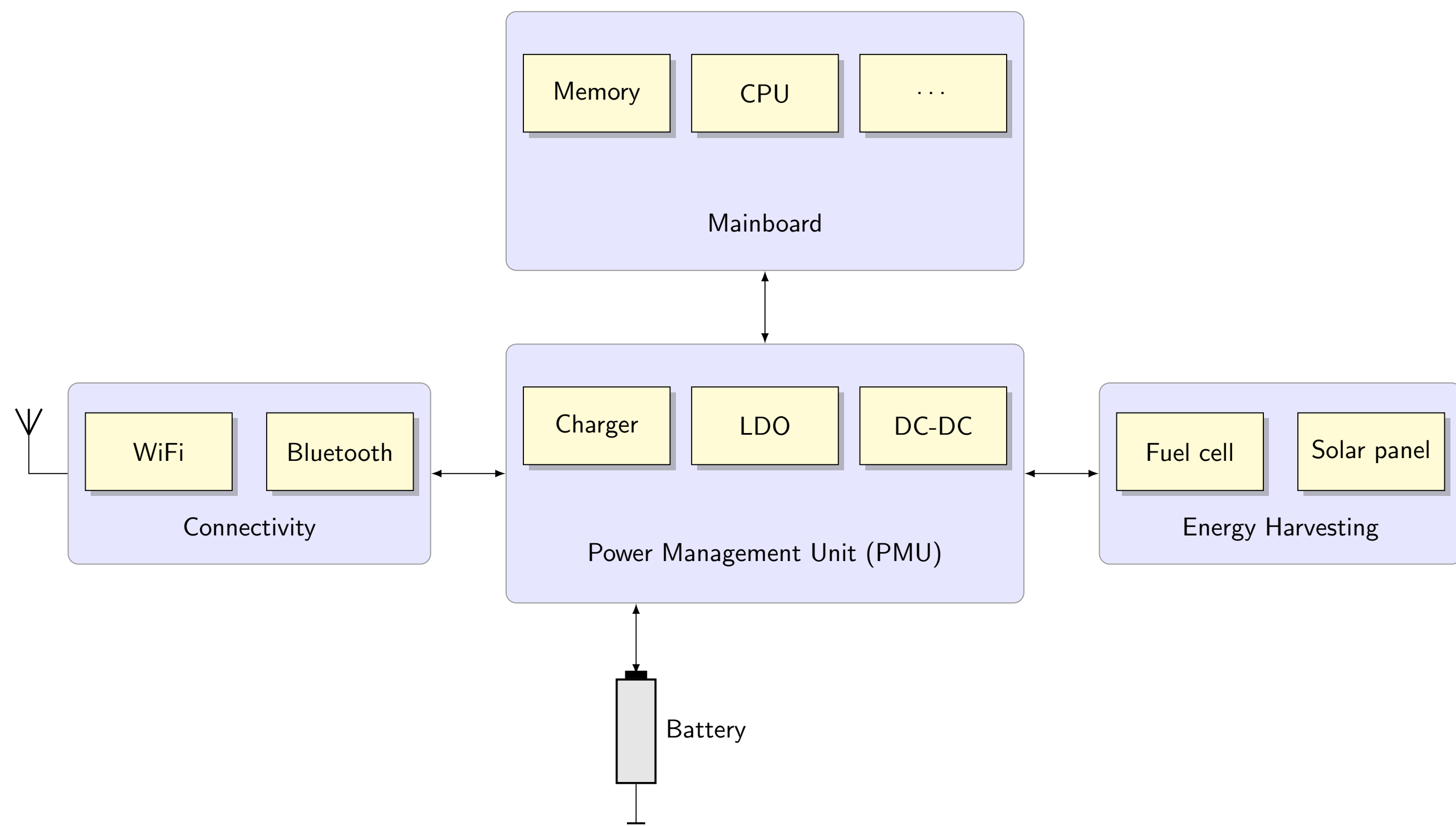
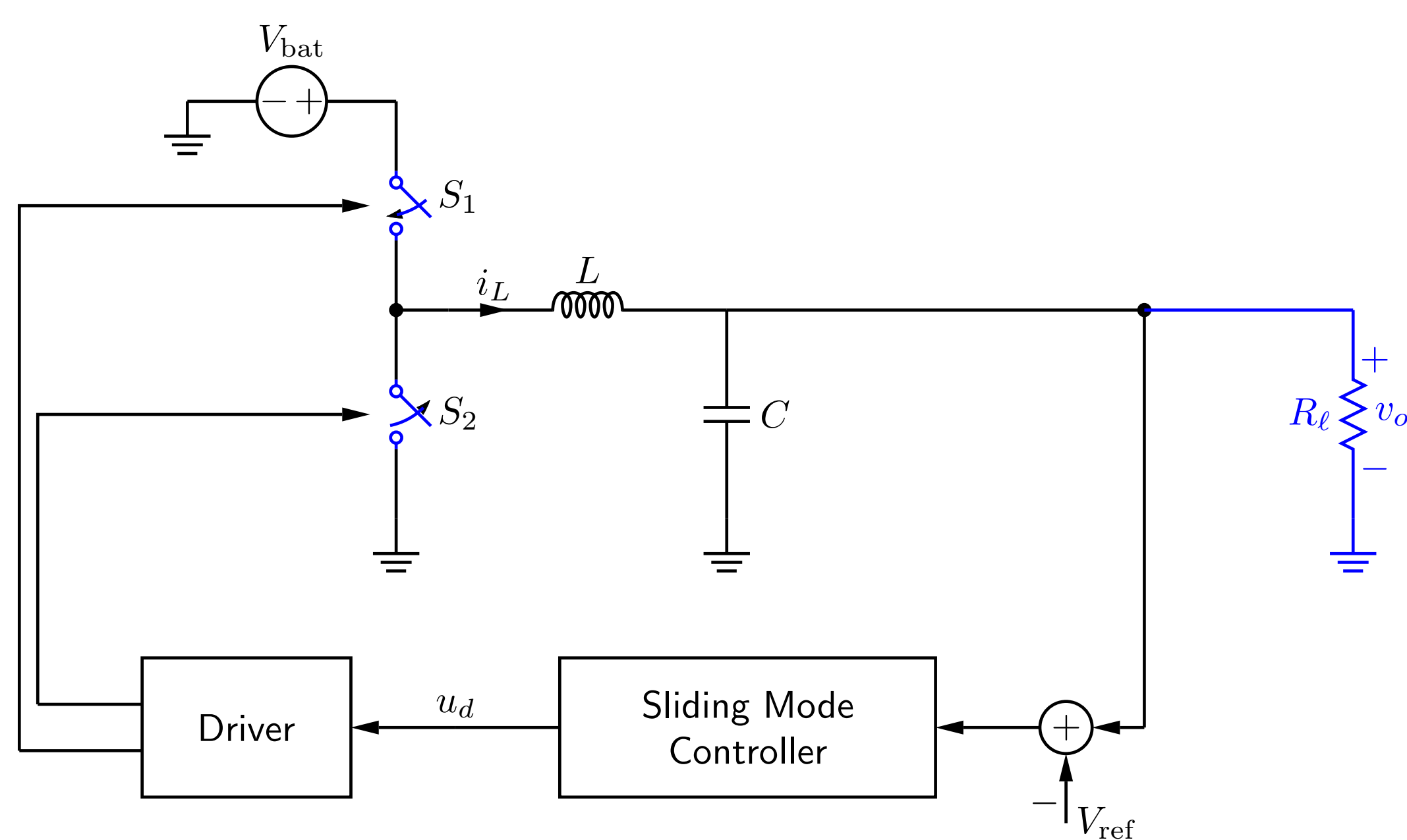


Motivation

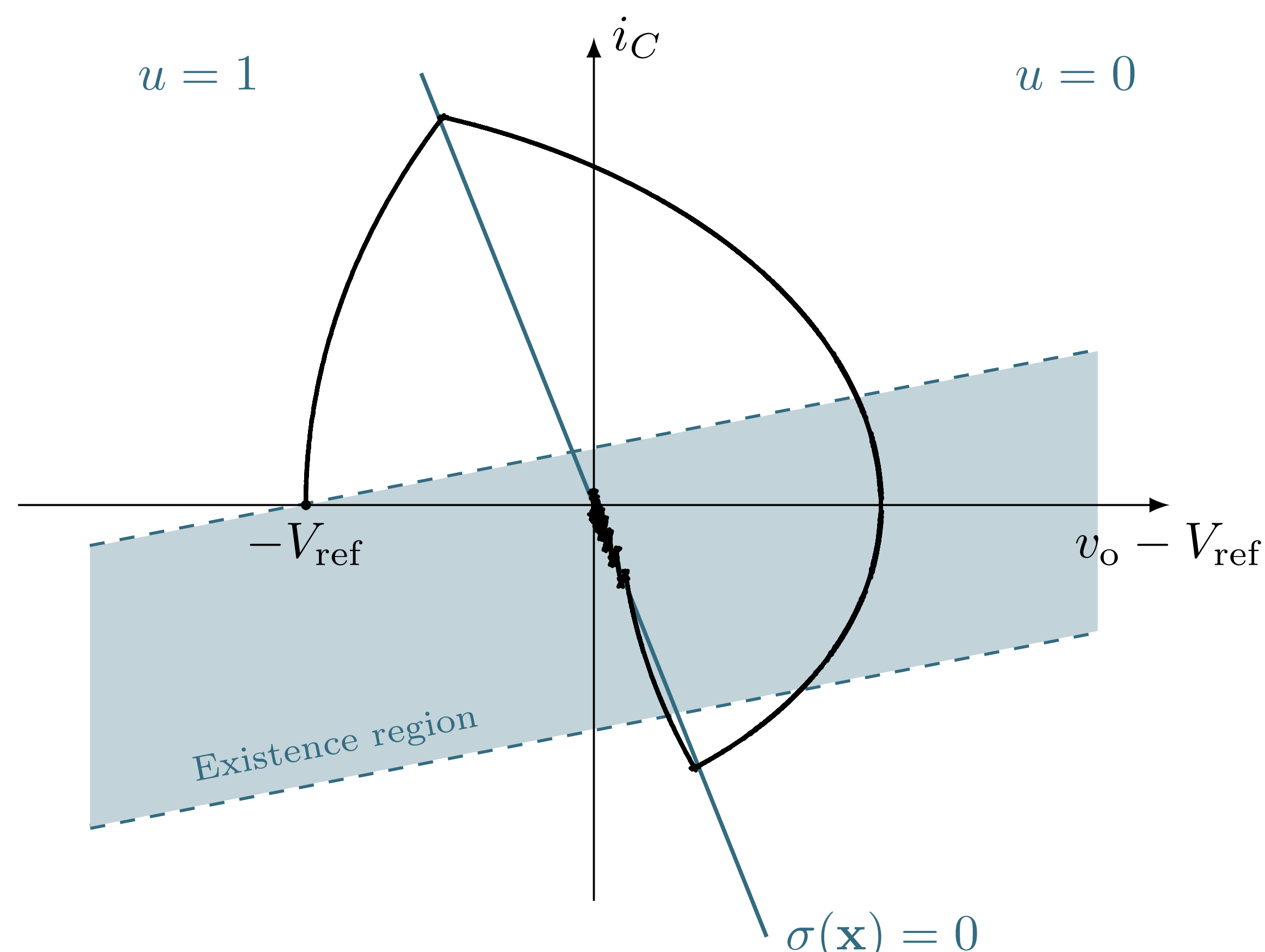


- Different components require different (**stable**) supply voltages
- Several DC-DC converters are typically necessary to convert the battery voltage into the different required levels
- Power conversion should be as efficient as possible
 - ▷ Resource savings
 - ▷ Extend lifetime of battery-powered devices

Control theory

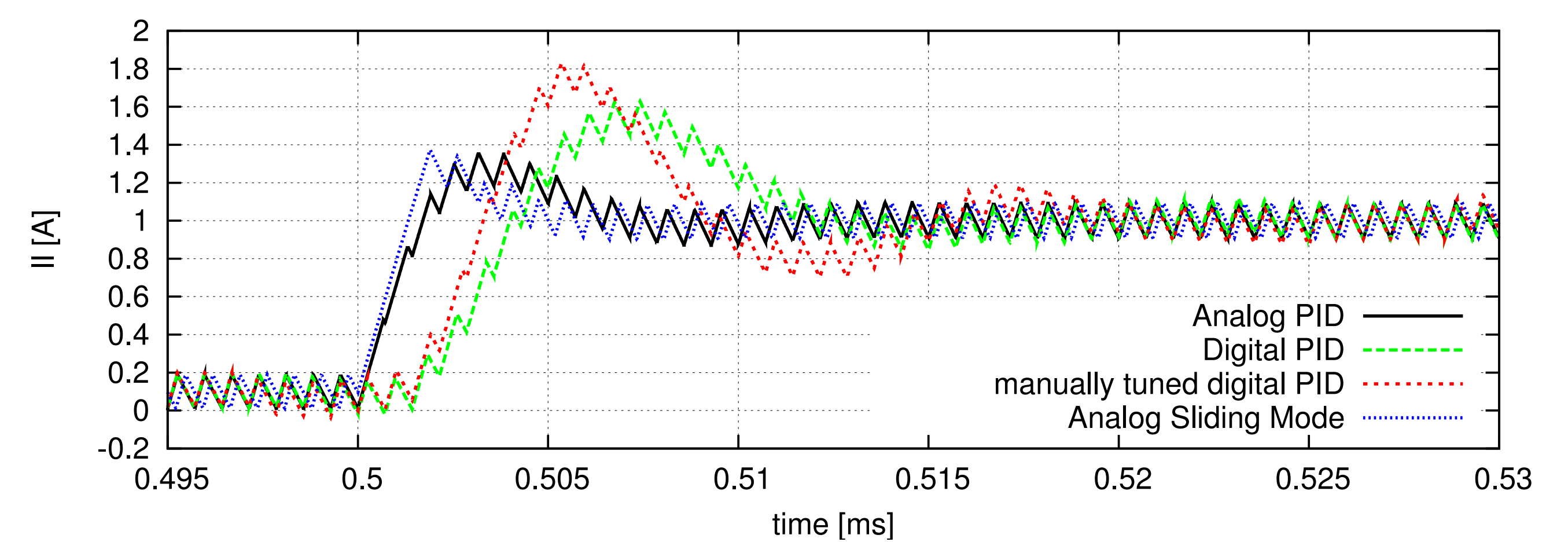
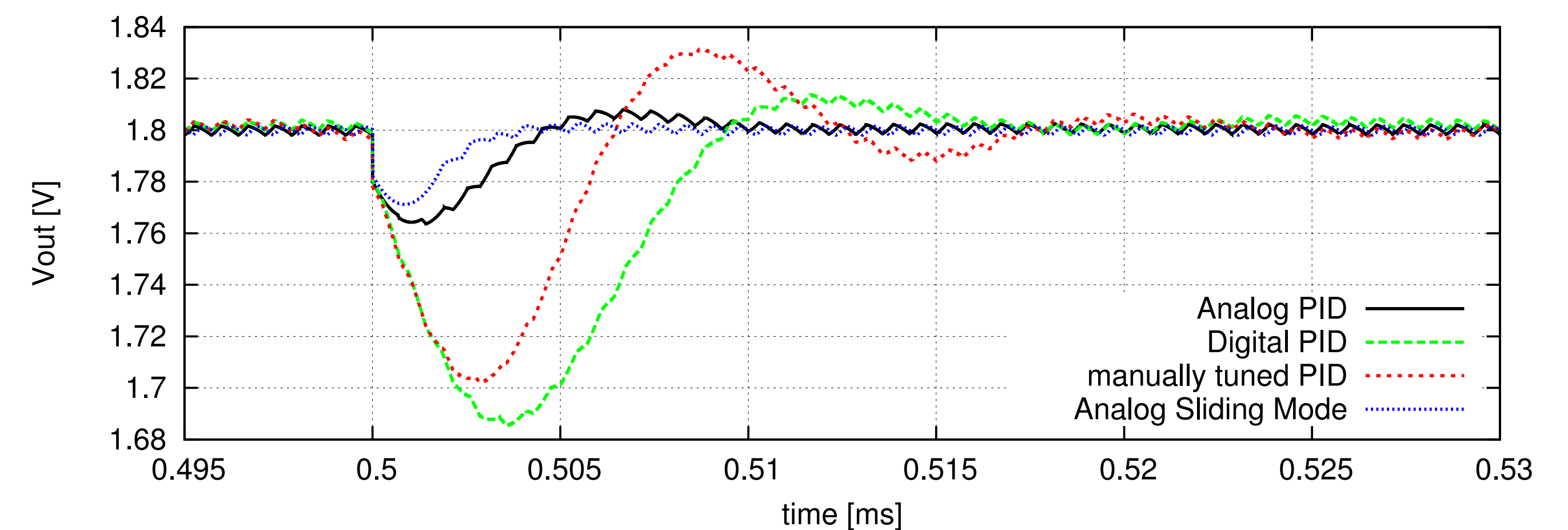


- Needed to tightly regulate the output voltage of a DC-DC converter
- Can play a key role in the efficiency
 - ▷ Reduce the switching activity (e.g. DCM, PFM)
 - ▷ Reduce under- and overshoots during transient \Rightarrow reference value can be reduced
- Alternative and modern structures alternative to linear PID worth exploring, e.g. Sliding Mode:
 - ▷ Improved dynamic performance (e.g. load transient)
 - ▷ Improved efficiency
 - ▷ Robustness to parameter variations



Implementation

- Choice between **analog** and **digital** should be made with care
- Digital implementation gives more **flexibility**
 - ▷ Power consumption of the additional ADC must be evaluated
 - ▷ Particularly critical at high switching frequencies
- Analog implementation generally offers faster dynamic response and lower power consumption



Challenges

- Maximize efficiency
- Optimize the control structures
 - ▷ Use innovative control techniques (especially with digital implementations)
 - Sliding mode
 - H_∞ control
 - Fuzzy logic
 - Predictive control
 - ▷ Auto-tuning of the control coefficients
- Fully integrated DC-DC converters
 - ▷ Output filter integrated on-chip
 - ▷ Requires extremely high switching frequencies
- Energy harvesting

Application example – Buck-Boost converter for mobile devices

- Capable of stepping up and down the input (battery) voltage
- Analog implementation for high-performance and low-power
- 0.13 μm CMOS technology
- Efficiency improvement via control algorithm (efficient operating mode selection)

Bibliography

- [1] "Special issue on digital control in power electronics," IEEE Transactions on Power Electronics, vol. 18, no. 1, pp. 293-503, 2003.
- [2] R. W. Erickson and D. Maksimovic, Fundamentals of Power Electronics, 2nd ed. Springer, 2001.
- [3] F. Kuttner et al., "A digitally controlled DC-DC converter for SoC in 28nm CMOS," in IEEE Solid-State Circuits Conference Digest of Technical Papers (ISSCC), 2011, vol. 18, no. 1, pp. 384-385.
- [4] L. Corradini, A. Babazadeh, A. Bjeletić, and D. Maksimović, "Current-Limited Time-Optimal Response in Digitally Controlled DC-DC Converters," IEEE Transactions on Power Electronics, vol. 25, no. 11, pp. 2869-2880, Nov. 2010.
- [5] S. Saggini, A. Costabeber, and P. Mattavelli, "A Simple Digital Autotuning For Analog Controller in SMPS," IEEE Transactions on Power Electronics, vol. 25, no. 8, pp. 2170-2178, Aug. 2010.
- [6] M. Agostinelli, R. Priewasser, S. Marsili, M. Huemer, "Fixed-Frequency Pseudo Sliding Mode Control for a Buck-Boost DC-DC Converter in Mobile Applications: a Comparison with a Linear PID Controller," in IEEE International Symposium on Circuits and Systems (ISCAS), 2011, pp. 1604 - 1607.